

# 7800 Series

## 3-Terminal Fixed Positive Voltage Regulator

### General Description

These voltage regulators are monolithic integrated circuits designed as fixed-voltage regulators for a wide variety of applications including local, on-card regulation. These regulators employ internal current limiting, thermal shutdown, and safe-area compensation. With adequate heatsink they can deliver output currents up to 1 ampere.

Although designed primarily as a fixed voltage regulator, these devices can be used with external components to obtain adjustable voltages and currents.

This series is offered in 3-pin TO-220, ITO-220 & TO-263 package.

#### TO-220



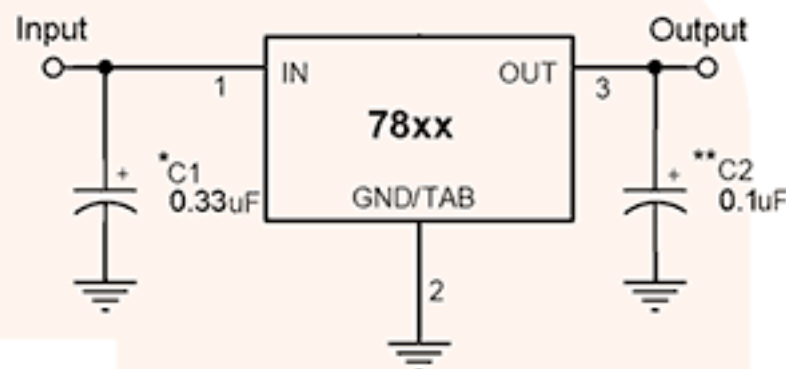
#### Pin Definition:

1. Input
2. Ground (tab)
3. Output

#### Features

- Output Voltage Range 5 to 24V
- Output current up to 1A
- No external components required
- Internal thermal overload protection
- Internal short-circuit current limiting
- Output transistor safe-area compensation
- Output voltage offered in 4% tolerance

#### Standard Application Circuit



A common ground is required between the input and the output voltages. The input voltage must remain typically 2.0V above the output voltage even during the low point on the input ripple voltage.

XX = these two digits of the type number indicate voltage.

\* = C<sub>in</sub> is required if regulator is located an appreciable distance from power supply filter.

\*\* = C<sub>o</sub> is not needed for stability; however, it does improve transient response.

#### Absolute Maximum Rating (T<sub>a</sub> = 25°C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Input Voltage	V <sub>IN</sub> *	35	V
Input Voltage	V <sub>IN</sub> **	40	V
Power Dissipation	P <sub>D</sub>	Internal Limited	W
Operating Junction Temperature	T <sub>J</sub>	0~+125	°C
Storage Temperature Range	T <sub>STG</sub>	-65~+150	°C

#### 7815 Electrical Characteristics

V<sub>in</sub>=23V, I<sub>out</sub>=500mA, 0°C ≤ T<sub>j</sub> ≤ 125°C, C<sub>in</sub>=0.33uF, C<sub>out</sub>=0.1uF; unless otherwise specified.)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit	
Output voltage	V <sub>out</sub>	T <sub>j</sub> =25°C	14.42	15	15.60	V	
		17.5V ≤ V <sub>in</sub> ≤ 30V, 10mA ≤ I <sub>out</sub> ≤ 1A, PD ≤ 15W	14.28	15	15.75		
Line Regulation	REG <sub>line</sub>	T <sub>j</sub> =25°C	17.5V ≤ V <sub>in</sub> ≤ 30V	--	12	300	mV
			18V ≤ V <sub>in</sub> ≤ 22V	--	3	150	
Load Regulation	REG <sub>load</sub>	T <sub>j</sub> =25°C	10mA ≤ I <sub>out</sub> ≤ 1A	--	12	300	mV
			250mA ≤ I <sub>out</sub> ≤ 750mA	--	4	150	
Quiescent Current	I <sub>q</sub>	T <sub>j</sub> =25°C, I <sub>out</sub> =0	--	4.3	8	mA	
Quiescent Current Change	ΔI <sub>q</sub>	17.5V ≤ V <sub>in</sub> ≤ 30V	--	--	1		
		10mA ≤ I <sub>out</sub> ≤ 1A	--	--	0.5		
Output Noise Voltage	V <sub>n</sub>	10Hz ≤ f ≤ 100KHz, T <sub>j</sub> =25°C	--	90	--	μV	
Ripple Rejection Ratio	RR	f=120Hz, 18V ≤ V <sub>in</sub> ≤ 28V	54	70	--	dB	
Voltage Drop	V <sub>drop</sub>	I <sub>out</sub> =1.0A, T <sub>j</sub> =25°C	--	2	--	V	
Output Resistance	R <sub>out</sub>	f=1KHz	--	19	--	mΩ	
Output Short Circuit Current	I <sub>os</sub>	T <sub>j</sub> =25°C	--	230	--	mA	
Peak Output Current	I <sub>o peak</sub>	T <sub>j</sub> =25°C	--	2.2	--	A	
Temperature Coefficient of Output Voltage	ΔV <sub>out</sub> / ΔT <sub>j</sub>	I <sub>out</sub> =10mA, 0°C ≤ T <sub>j</sub> ≤ 125°C	--	-1	--	mV/°C	